

FIG.1 (PRIOR ART)

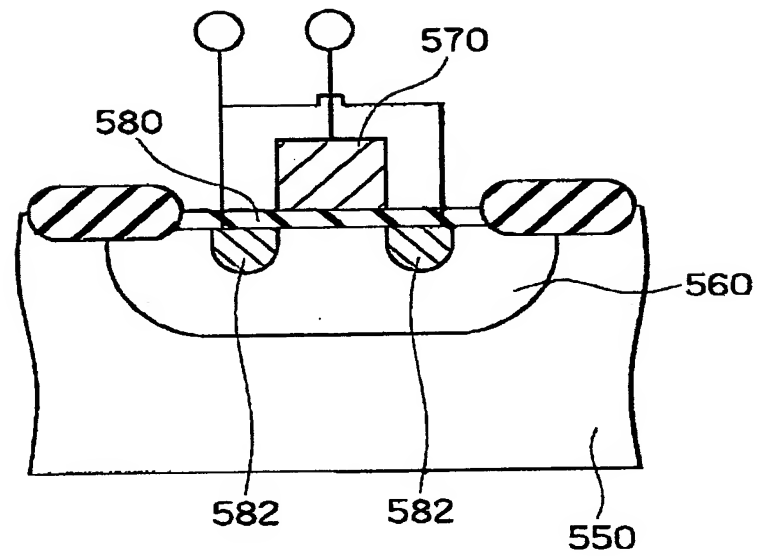


FIG.2 (PRIOR ART)

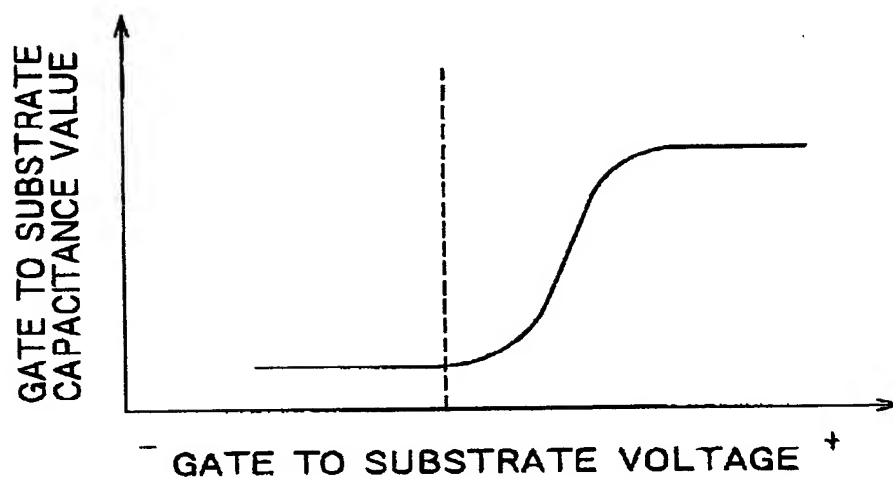


FIG.3 (PRIOR ART)

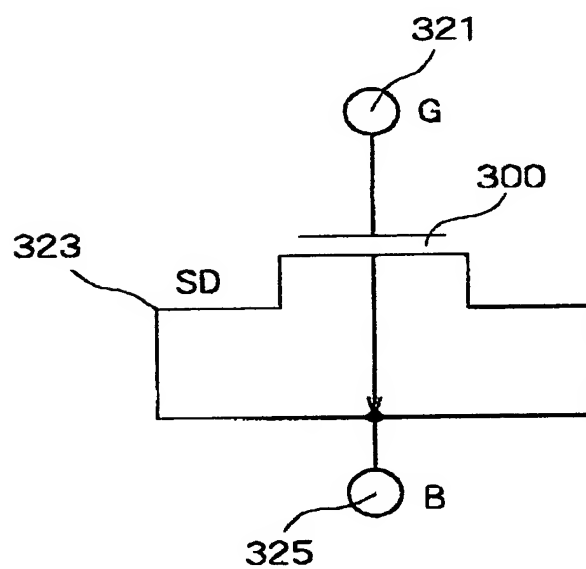
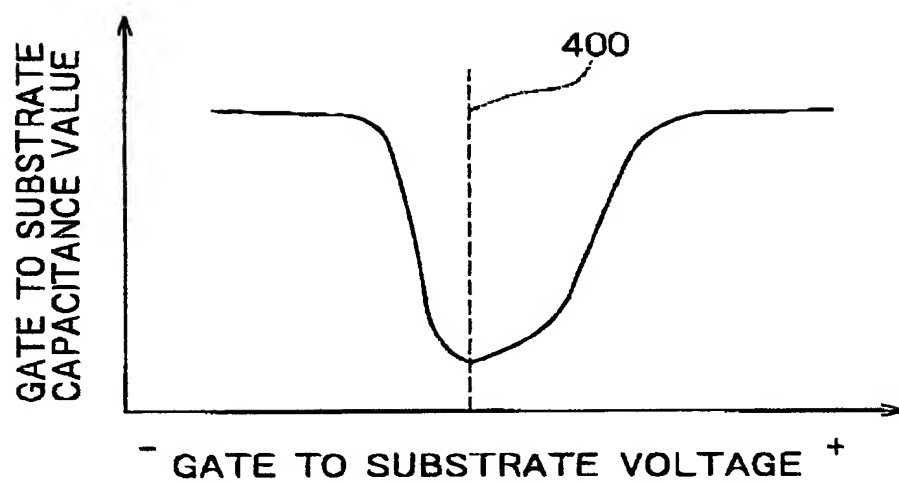


FIG. 4 (PRIOR ART)



A graph showing the relationship between Gate to Substrate Capacitance Value (Y-axis) and Gate to Substrate Voltage (X-axis). The X-axis is labeled with a minus sign (-) on the left and a plus sign (+) on the right. The curve shows a minimum capacitance value in the middle, flanked by two regions of increasing capacitance. Key points on the curve are labeled: 610 (left side), 400 (middle), 600 (right side), and 660 (far right). A horizontal line with arrows at both ends is drawn across the graph, and a large arrow points to the left, indicating a shift or transition.

FIG. 7

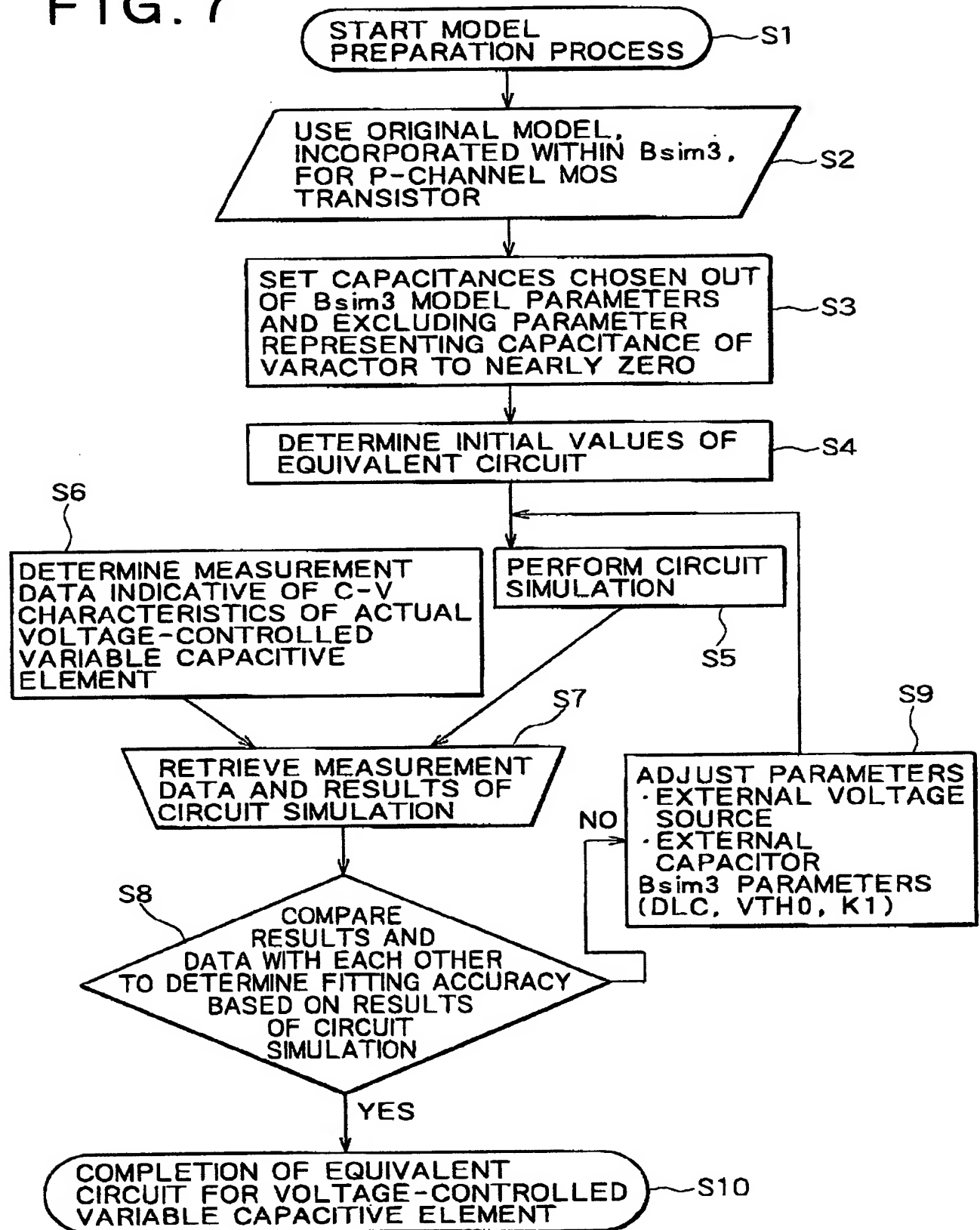


FIG. 8

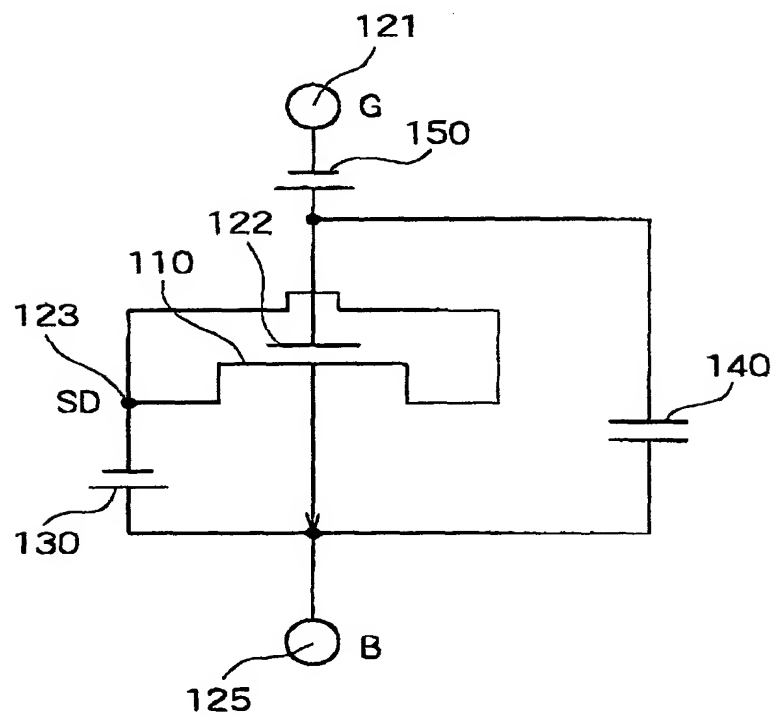


FIG. 9

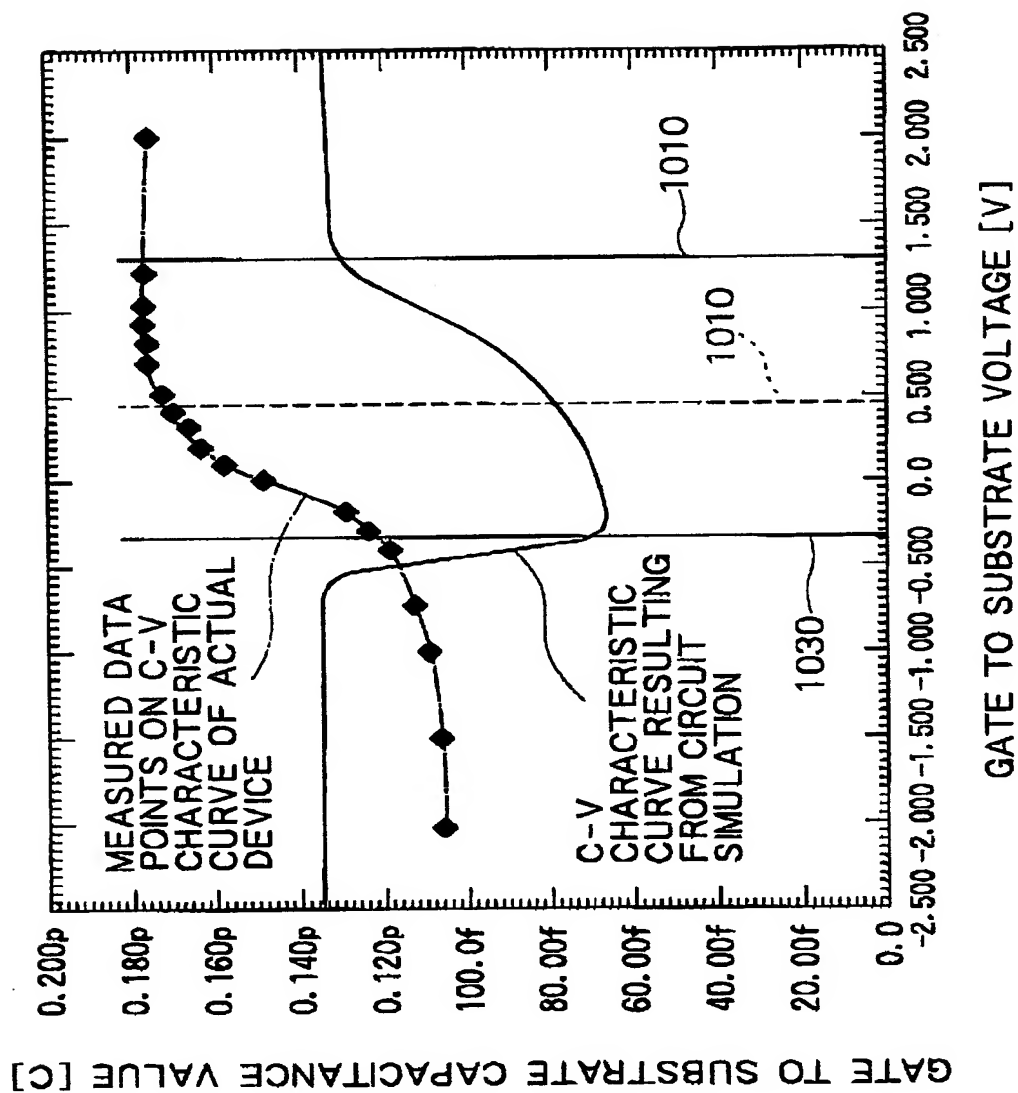


FIG. 10

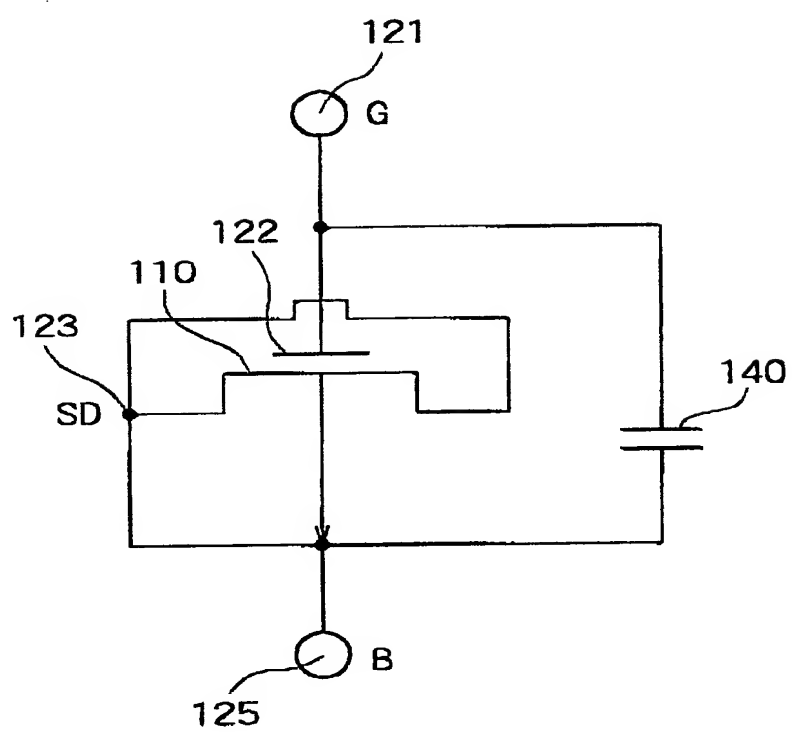




FIG. 11

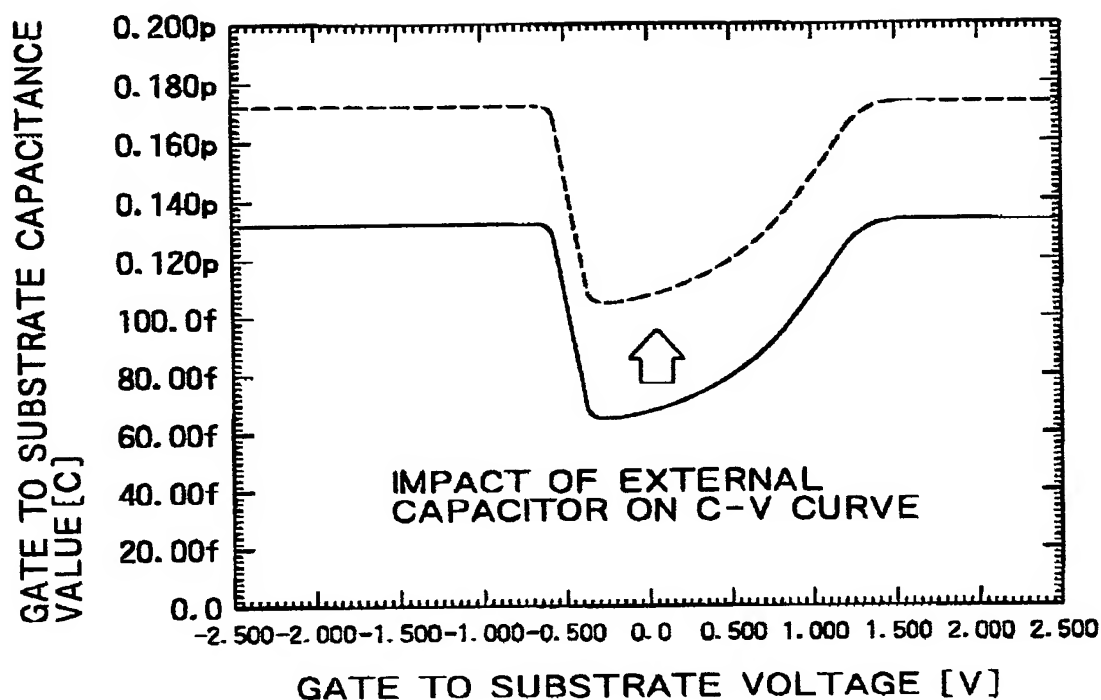
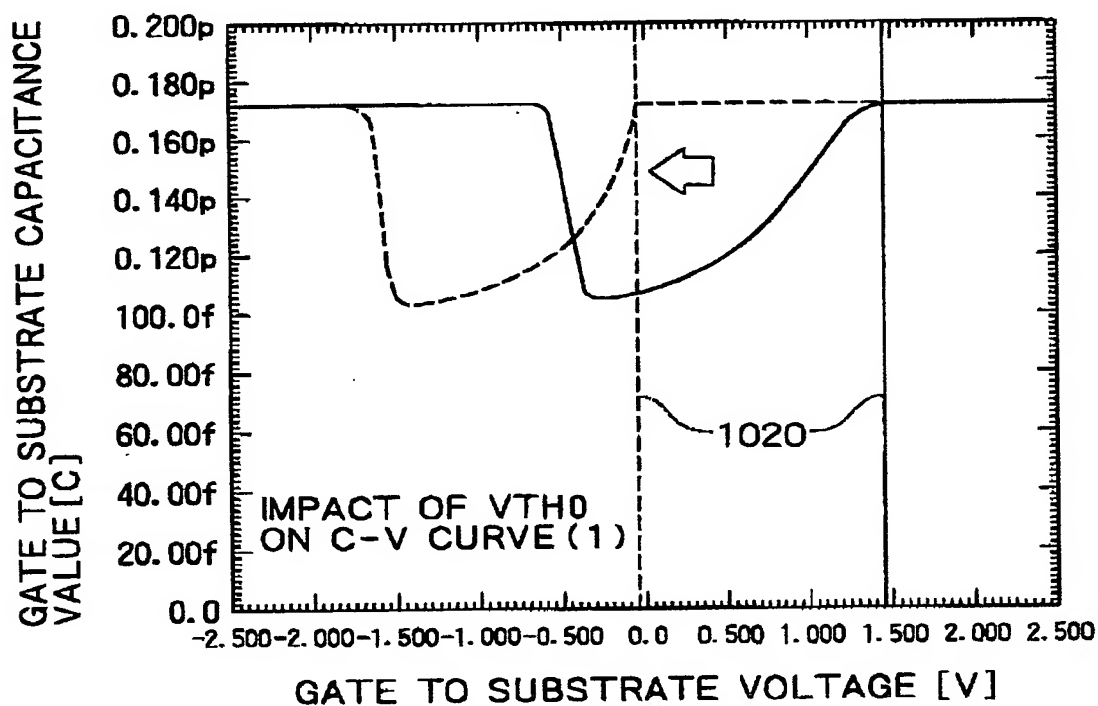
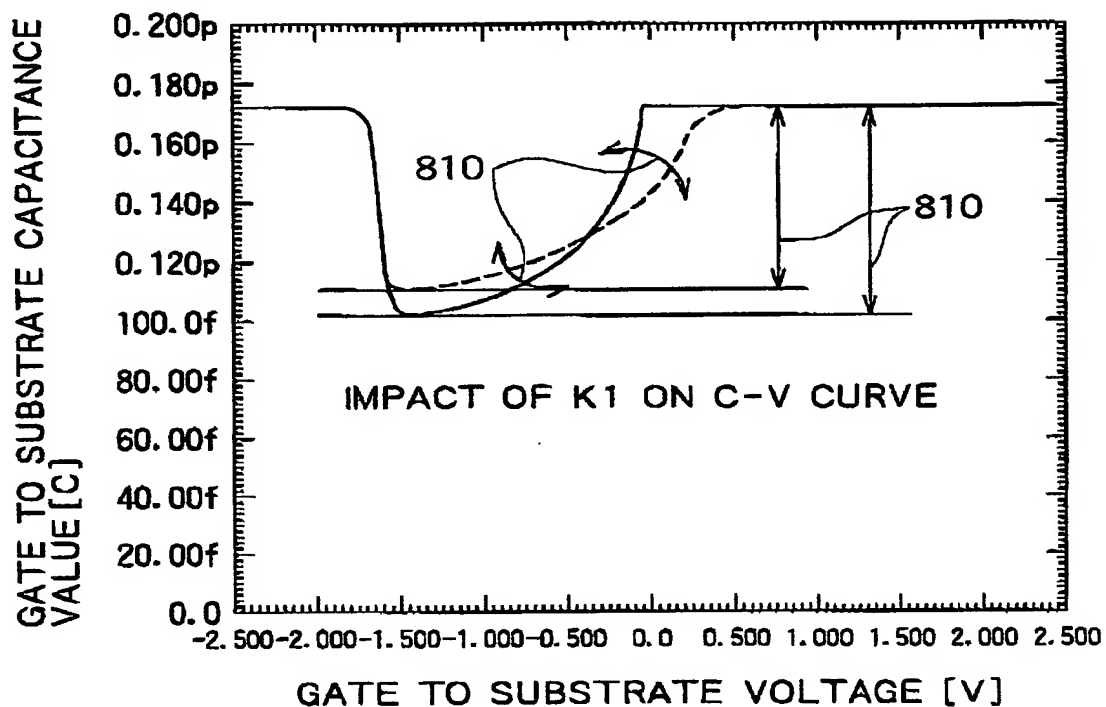


FIG. 12



# FIG. 13



# FIG. 14

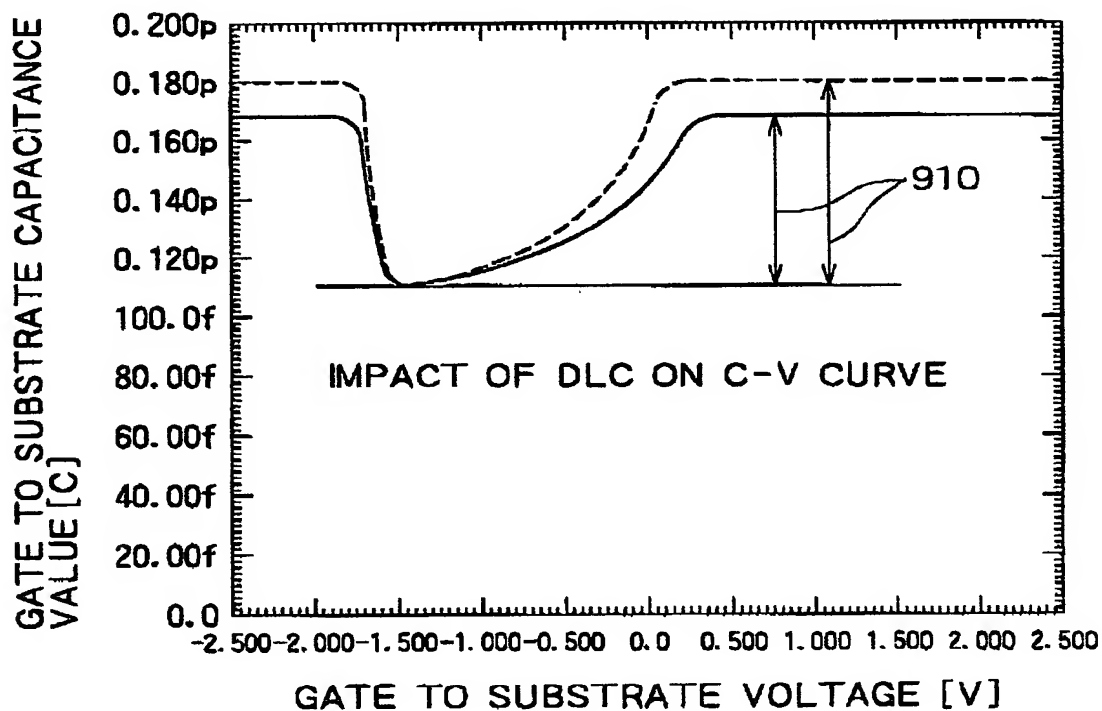


FIG. 15

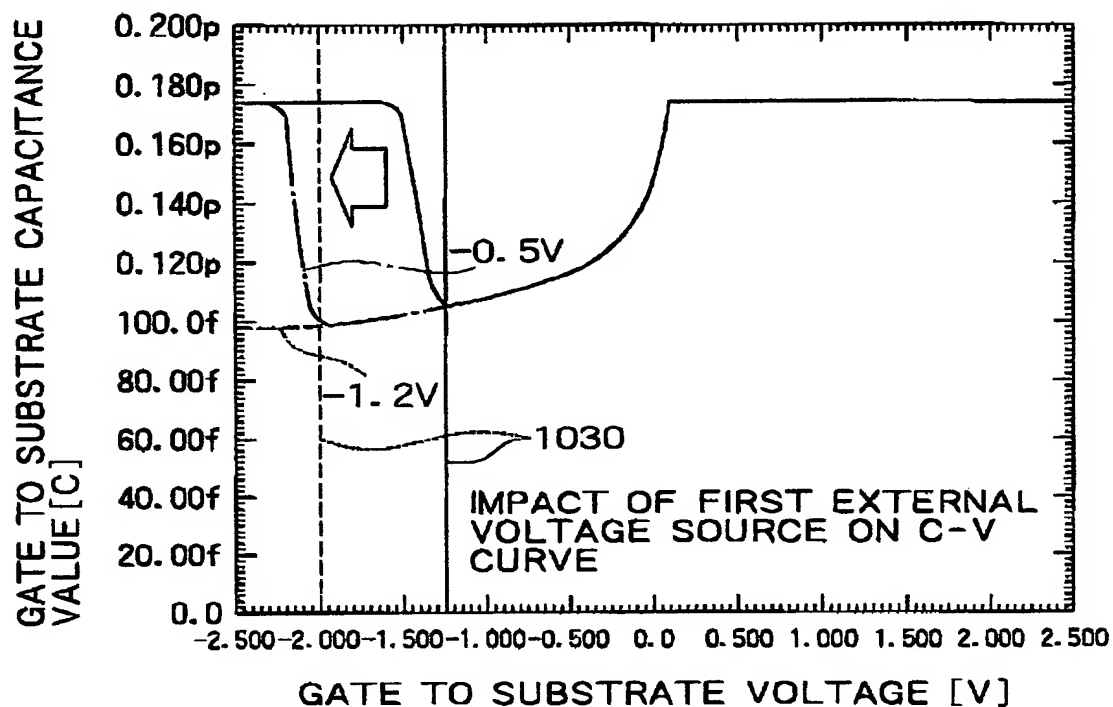
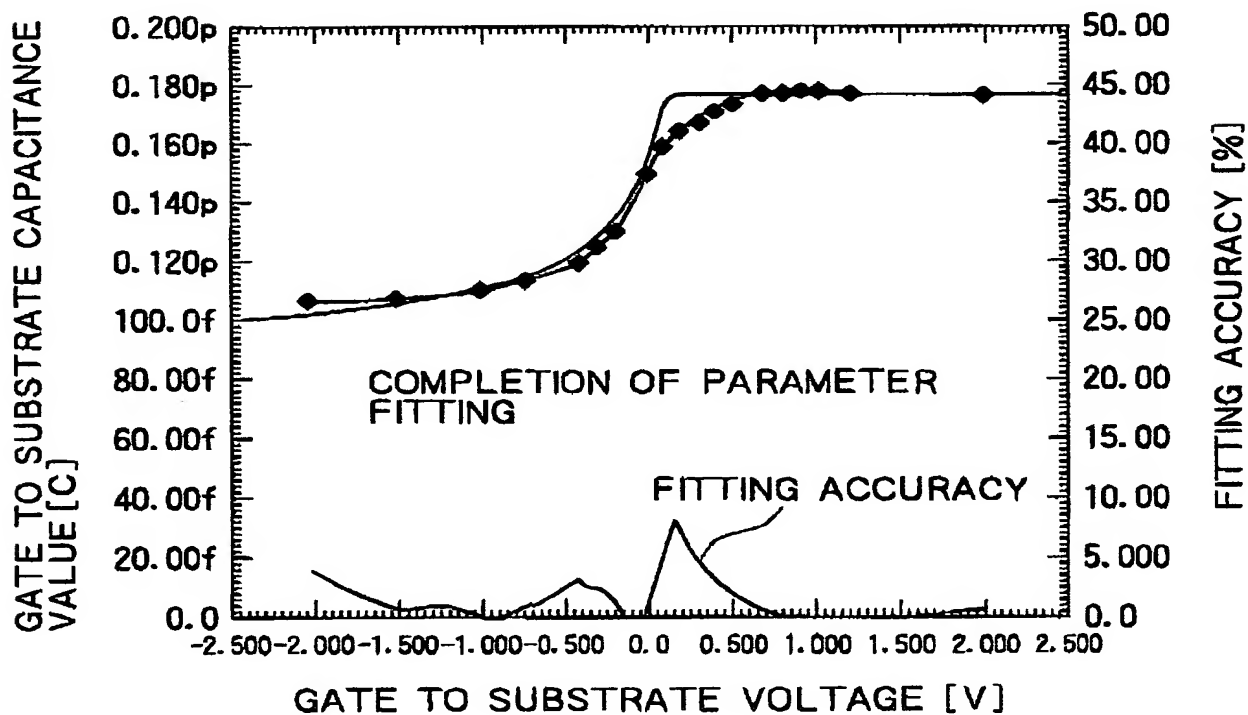
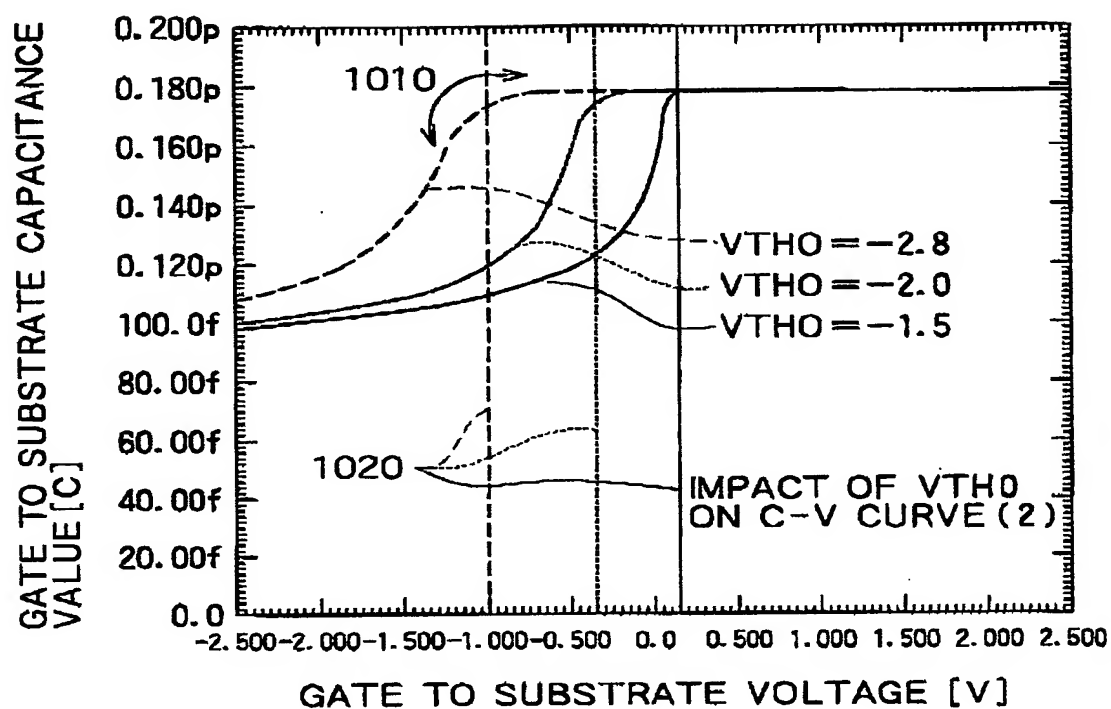


FIG. 16



# FIG. 17



# FIG. 18

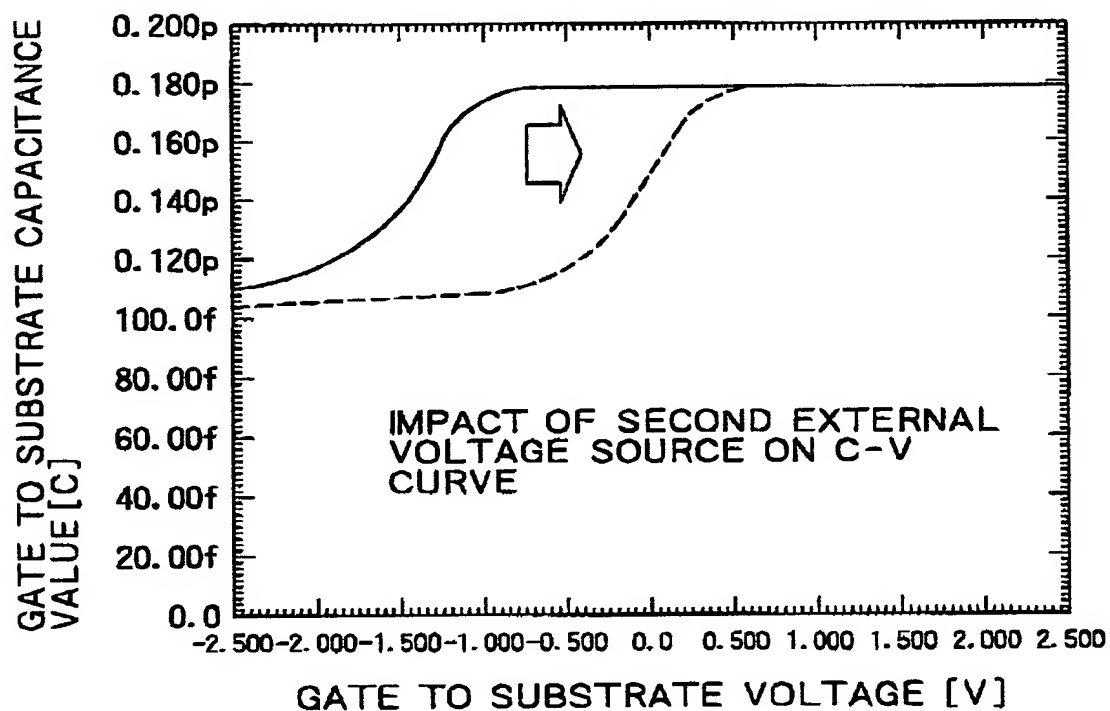


FIG. 19

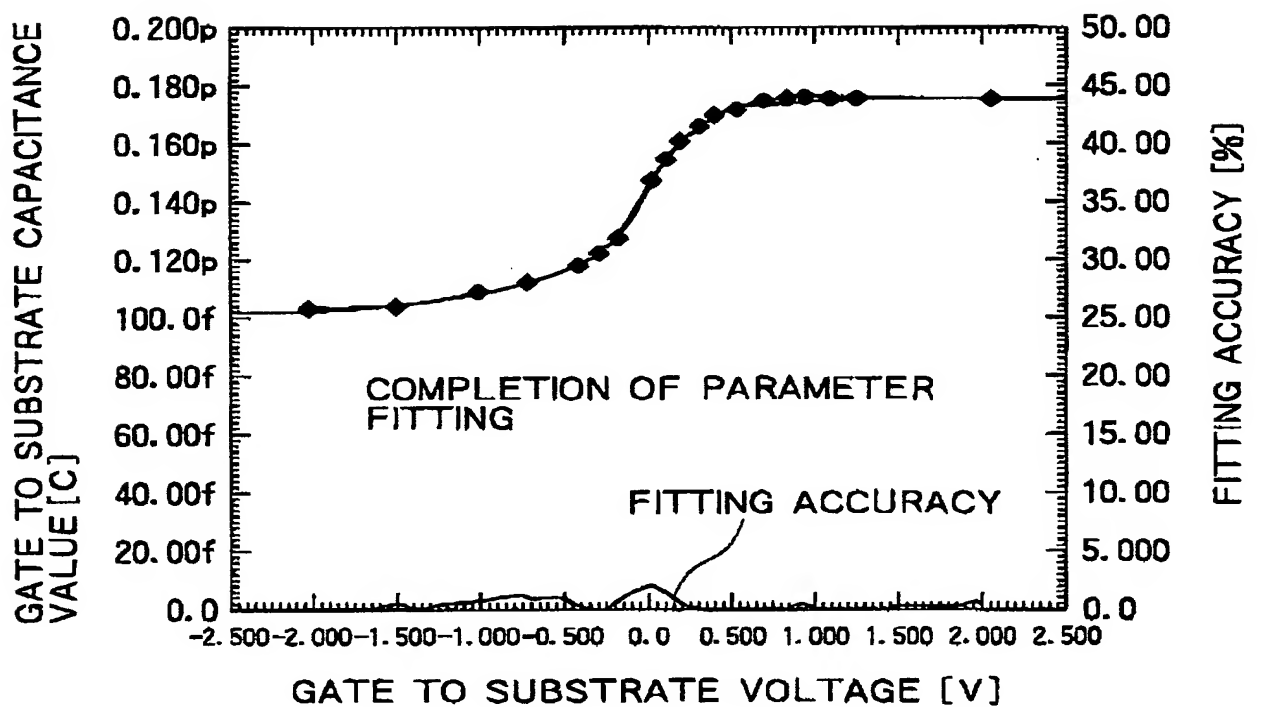


FIG. 20

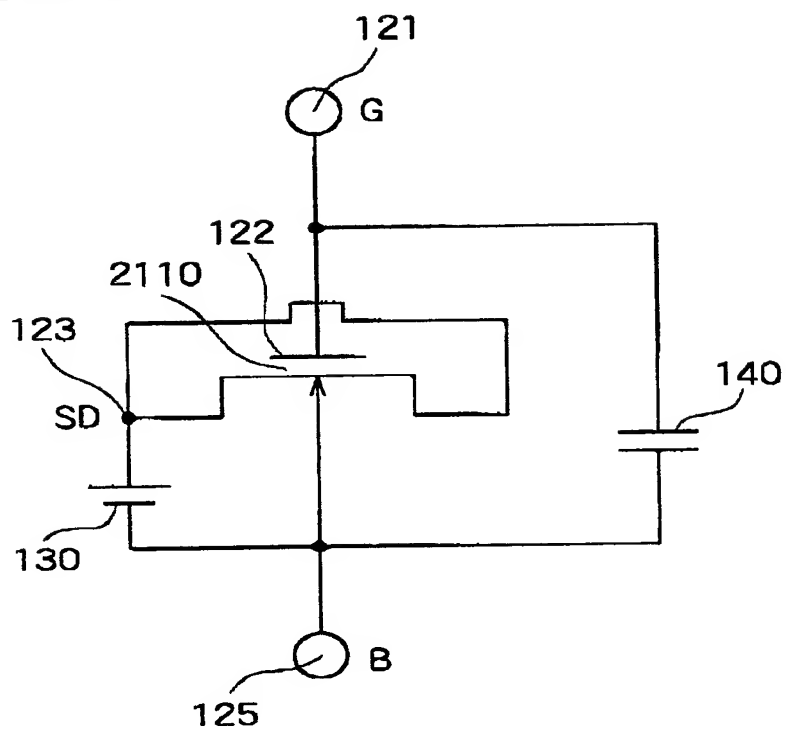


FIG. 21

